

In the claims

1. (Currently amended) A driver circuit for driving simultaneously a variable number of firing resistors for a printhead during a printing firing cycle, the driver circuit comprising:

a drive circuit for supplying firing pulses for firing the variable number of firing resistors during the printing firing cycle, the driver circuit being a voltage source supplying a voltage of a drive signal encompassing the firing pulses, the voltage having a predetermined magnitude;

a circuit for adjusting ~~[[a]]~~ the predetermined magnitude of ~~[[a]]~~ the voltage ~~or a current~~ of said drive signal during the printing firing cycle in dependence on the variable number of firing resistors to be fired simultaneously in a given subset during the printing firing cycle,

wherein the circuit applies a data variable offset voltage dependent on the variable number of firing resistors, and a fixed offset voltage not dependent on the variable number of firing resistors.

2. (Cancelled)

3. (Currently amended) The driver circuit of Claim ~~[[2]]~~ 1, wherein said circuit adjusts the ~~voltage~~ predetermined magnitude of ~~the voltage~~ in dependence on said variable number of firing resistors being simultaneously fired.

4. (Original) The driver circuit of Claim 3, wherein said circuit provides an increased voltage magnitude for larger variable numbers.

5. (Cancelled)

6. (Currently amended) The driver circuit of Claim [[5]] 1, wherein said fixed offset voltage is inversely proportional to the variable number of firing resistors.

7. (Currently amended) The driver circuit of Claim [[2]] 1, wherein said fixed offset voltage is a monotonically increasing function of said variable number of firing resistors.

8.-12. (Cancelled)

13. (Currently amended) A driver circuit for driving simultaneously a variable number of firing resistors for a printhead, the driver circuit comprising:

a drive circuit for supplying a drive signal for firing the variable number of firing resistors during a printing firing cycle, the drive circuit including a voltage source providing a voltage of the drive signal, the voltage having a predetermined magnitude;

means for adjusting [[a]] the predetermined magnitude of [[a]] the voltage ~~or a current~~ of said drive signal during the printing firing cycle in dependence on the variable number of firing resistors to be fired simultaneously in a given subset during the printing firing cycle,

wherein said adjusting means comprises circuit means for providing a data variable offset voltage dependent on said variable number of firing resistors and a fixed offset voltage not dependent on said variable number of firing resistors.

14. (Cancelled)

15. (Currently amended) The driver circuit of Claim [[14]] 13, wherein said adjusting means comprises means for adjusting the ~~voltage~~ predetermined magnitude of the voltage in dependence on said variable number of firing resistors being simultaneously fired.

16. (Original) The driver circuit of Claim 15, wherein said adjusting means provides an increased voltage magnitude for larger variable numbers.

17. (Cancelled)

18. (Currently amended) The driver circuit of Claim ~~[[17]]~~ 13, wherein said offset voltage is inversely proportional to the variable number of firing resistors.

19. (Original) The driver circuit of Claim 13, wherein said magnitude is a monotonically increasing function of said variable number of firing resistors.

20.-24. (Cancelled)